IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Chen et al.

Serial No.: Not Yet Assigned

Group Art Unit: Unknown

Filing Date: Concurrently Herewith

Examiner: Unknown

For:

A SYSTEM AND METHOD FOR REDUCING DATA LOSS IN DISK ARRAYS

BY ESTABLISHING DATA REDUNDANCY ON DEMAND

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0441.

Respectfully submitted,

Mulu S. Rhun

Mohammad S. Rahman Registration No. 43,029

Date: Name of the Position of

Docket Number (Optional)	Application Number		
ARC920030067US1	Not Yet Assigned		
Applicant(s)			
Applicant(s) Chen et al.			
Filing Date	Group Art Unit		

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				U.S. PAI	ENT DOCUMENTS		 	Γ		
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	1	G DATE OPRIATE	
		5,467,361	11/14/95	Shipma	n, Jr.					
		5,490,248	02/06/96	Dan et	al.				_	
		5,566,316	10/15/96	Fechne	r et al.					
		5,737,344	04/07/98	Belser e	et al.					
		5,893,919	04/13/99	Sarkozy	y et al.					
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6,490,683		6,490,683	12/03/02	Yamada et al.						
	6,587,977 01		07/01/03	Riggle et al.						
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	REF	DOCUMENT NUMBER	DATE		COUNTRY		SUBCLASS	Translation		
_			20110101	1_			+	YES NO		
		JP2001216098A	08/10/01	Japan						
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		Iwata et al., ''Raid Dis February, 1996, pp. 30	k Arrays with Ro 1-302.	edundant Co	onfiguration Data'', IB	M Technical Disc	losure Bulletin	, Vol. 39,	No. 2,	
	_	P.S. Yu. Efficient Add	ressing Scheme fo	or Clustered	l Redundant Array of I	ndenendent Disk	s. " IBM Techi	nical Discl	osure	
		Bulletin, Vol. 36, No. 6	P.S. Yu, Efficient Addressing Scheme for Clustered Redundant Array of Independent Disks, "IBM Technical Disclosure Bulletin, Vol. 36, No. 6B, June, 1993, pp. 39-43.							
EXAMINE	R				DATE CONSIDERED					
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.